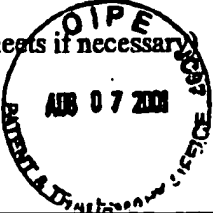


Form PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 7-80) PATENT AND TRADEMARK OFFICE LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)		Atty. Docket No. YOR920000587US1 (13958)		Serial No. 09/827,073 <div style="text-align: center; border: 1px solid black; padding: 5px;"> RECEIVED AUG 09 2001 </div>			
		Applicant Louis L. Hsu et al.		Technology Center 2100			
		Filing Date April 5, 2001		Group 2818			
U.S. PATENT DOCUMENTS							
EXAMINE R INITIAL*		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (if appropriate)
		AA					
		AB					
		AC					
		AD					
		AE					
		AF					
		AG					
		AH					
		AI					
		AJ					
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
TP		"An 8ns Random Cycle Embedded RAM Macro with Dual-Port Interleaved DRAM Architecture (D ² RAM)", Yasuhiro Agata et al, 2000 IEEE International Solid-State Circuits Conference, 9 pages.					
EXAMINER		Trong Phan		DATE CONSIDERED		4/9/04	
* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							